For creating the subtractor :  
(for the truth table , refer to the picture attached in the folder )

Currently we have adders till 64 bits   
  
for subtraction we can use the 2’s complement Method :

For input B   
1. Take the XOR of all the bits with 1 ( flip all the bits)

2.Set the carry-in = 1

3. Add the modified B and the input A using the Created Adder modules.

We have made the test bench for 4 bit and 8 bit subtractor by this method.

Refer to test\_bench folder. For test bench